

Digital IC Trainer (TTL) has been designed for verification of the truth table of logic gates and address etc. Students can be trained about the digital ICs by using this board. The fundamentals of the digital ICs can easily be understood by performing required experiments for verification of truth table of logic gates. Quite large area of the bread board has been provided on the panel for ICs. Students can make the circuits easily on the bread board with the help of other accessories which have been provided on the panel.

Practical experience on this board carries great educative value for Science and Engineering Students.



#### SPECIFICATIONS

- \* OUTPUT D.C. VOLTAGE : Fixed 5V  $\pm$ 1%.
- \* OUTPUT CURRENT : 1.5 Amp.
- \* LOAD REGULATION (NO LOAD TO FULL LOAD) :  $\pm$ 1% of the highest specified output voltage.
- \* RIPPLE AND NOISE : less than 5 mV.
- \* VARIABLE CLOCK FREQUENCY : 1 Hz - 100 Hz  
100 Hz - 10 KHz  
10 KHz - 1 MHz
- \* LOGIC INPUTS : Four switches for High/Low.
- \* OUTPUT INDICATORS : Two 5 mm bright Red LEDs.
- \* SEVEN SEGMENT DISPLAY : "Common Anode" type Red LED.
- \* BREAD BOARD : Half main strip for connections &  
One full main strip each set for ICs.
- \* OPERATING CONDITIONS : 0 to 40°C and 95% R.H. at 40°C.
- \* LOGIC GATES PROVIDED : OR (7432), AND (7408), NOT (7404) NAND (7400), NOR (7402) & EX-OR (7486)
- \* EXTRA FEATURES : Other ICs can also be used to perform required experiments with any gates/ address etc.
- \* INPUT VOLTAGE : 230 V  $\pm$ 10% at 50 Hz A.C. Mains.
- \* Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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