



38697 Parallel Adder and Subtractor Trainer is a ready to use digital electronics experiment trainer. This product, has been designed specifically for the students to understand the concept of parallel binary addition and subtraction methods for 4-bit and 8-bit binary numbers.

A Parallel Adder is a combinational logic circuit which has various registers to input the data and thus generate the output. Because of this, input bits are applied simultaneously and the sum appears at the output almost immediately. The parallel subtractor circuit works in similar manner where the difference appears as soon as the inputs are applied.

This product has logic switches with LEDs for logic high and low input facility.

#### OBJECT

- 01 To study 4-bit binary addition & subtraction operation
- 02 To study 8-bit binary addition & subtraction operation

#### FEATURES

- 01 4-bit/8-bit Binary Addition & Subtraction  
Easy switching between addition and subtraction modes
- 03 LEDs for visual indication of input-output & carry out logics

#### TECHNICAL SPECIFICATION

- 01 DC Power Supply : +5 V DC
- 02 Logic levels  
+5 V : High (logic 1)  
0V : Low (logic 0)
- 03 LED indication : LED will be ON for logic high or '1' state and  
Will be OFF for Logic low or '0' state
- 04 Weight : 1 KG (Approx)
- 05 Dimensions (mm) : W340 X H125 X D210

#### LIST OF ACCESSORIES

- 01 Patch Cord 2mm length 50 cm Red-----14
- 02 Patch Cord 2mm length 50cm Black-----13

Note: Specifications are subject to change.

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