

Advance Level Logic Trainer has been designed specifically for the study of Re-triggerble monostable Multivibrator, Synchronous counting, Up and down counting, Serial to Parallel data converting. Writing and reading data from Random Access Memory (RAM) and concepts of input output bus.

Practical experience on this board carries great educative value for Science and Engineering Students.

OBJECT

To design, fabricate and test the following:

1. To study Re-triggerble Monostable Multivibrator using IC 555 and output through LED.

2. Synchronous Counters:

- 2.1. 4-Stage binary synchronous up-counter with parallel carry.
- 2.2. 4-Stage binary synchronous down-counter with parallel carry.
- 2.3. 4-Stage binary synchronous up-counter with series carry.
- 2.4. 4-Stage binary synchronous down-counter with series carry.
- 2.5. 4-Stage binary synchronous up-down counter with Parallel carry.
- 2.6. 4-Stage synchronous up-down counter with series carry.
- 2.7. 4-Stage synchronous decade counter with parallel carry.

3. Series Parallel Counters:

- 3.1. 3-Stage mod-5 series parallel counter.
- 3.2. 4-Stage mod-10 series parallel counter.

4. Serial to Parallel Data Converter

5. 1024 X 4 Bit Static Random Access Memory (2114)

- 5.1. To study the Write operation of 1024 X 4 Bit Random Access Memory.
- 5.2. To study the Read operation of 1024 X 4 Bit Random Access Memory.

6. 8212 - Single Input/ Output Port

- 6.1 To Study Input and output modes of IC 8212 I/O Port.

FEATURES

The board consists of the following built-in parts:

01. + 5V D.C. at 200mA, IC Regulated Power Supply Internally connected.
 02. +10V D.C. at 100mA, IC Regulated Power Supply internally connected.
 03. 1 KHz Square Wave Generator.
 04. Switches to set Data & Address.
 05. LEDs for visual indication of Address used Data conditions.
 06. Four, J-K master slave flip-flops with preset and clear arrangement.
 07. Five, 2-input NAND gates.
 08. Four, 4-input NAND gates.
 09. Pulser switch for clear arrangement.
 10. LEDs for visual indication of output status of each flip-flop.
 11. Single I/O port IC 8212.
 12. Tristate buffer IC 74244.
 13. Transistor BC 177.
 14. SPDT switches for logic selection.
 15. Adequate no. of other Electronic Components.
 16. Mains ON/OFF switch, Fuse and Jewel light.
- * The unit is operative on 230V \pm 10% at 50Hz AC. Mains.
 * Adequate no. of patch cords stackable from rear both ends 2mm spring loaded plug length 1/2 metre.
 * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
 * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

OTHER APPARATUS REQUIRED

- * Dual Trace Cathode Ray Oscilloscope 20 MHz (Optional).



Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

IT-2013, Ramchandrapura Industrial Area, Sitapura Extension,
Near Bombay Hospital, Vidhani Circle, Jaipur-302022, Rajasthan, India,
Tel: +91-141-2771791 / 2771792; Email: info@tesca.in, tesca.technologies@gmail.com
Website: www.tesca.in