

Computer Logic Training Board has been designed specifically for the study of NAND gate Encoder, to encode Decimal number to BCD(binary coded) equivalent. This Training Board gives a better understanding of the phenomena of encoder using four input NAND gates with ten decimal inputs and 4-bit binary output. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.



**Object:**

To demonstrate decimal to BCD (binary coded) encoder using 4-input NAND gates and verify the encoding.

**Features:**

The board consists of the following built-in parts :

01. +5V D.C. at 200mA, IC regulated power supply internally connected.
  02. Three, Dual 4- Input NAND gate ICs.
  03. LEDs (14 nos.) for visual indication of status.
  04. Miniature SPDT switches (10 nos.) for Input selection.
  05. Adequate no. of other Electronic Components.
  06. Mains ON/OFF switch, Fuse and Jewel light.
- \* The unit is operative on 230V  $\pm$ 10% at 50Hz A.C. Mains.
  - \* Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
  - \* Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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