

Computer Logic Training Board on 1024 X 1 Bit Static Random Access Memory (2102) has been designed specifically to get the familiarization of the operation of static MOS memory. The unit is self contained and provide the understanding of Read, Write and Chip Enable operation with the help of switches and LEDs. Dynamic operation of the chip can also be understood by connecting the pulse generator at the appropriate terminal.

Practical experience on this board carries great educative value for Science and Engineering Students.

**Object:**

To study Operation of a 1024 X 1 Bit Static Random Access Memory.

Features:

The board consists of the following built-in parts :

01. + 5V D.C. at 50mA, IC regulated power supply internally connected.
 02. Switches to set the memory data and address.
 03. LEDs for visual indication of output data (read out).
 04. Logic indication terminal provided to check the logic state of various pins of IC.
 05. Adequate no. of other Electronic Components.
 06. Mains ON/OFF switch, Fuse and Jewel light.
- * The unit is operative on 230V \pm 10% at 50Hz A.C. Mains.
 - * Adequate nos. of patch cords stackable from rear both ends 4mm spring loaded plug, length ½ metre
 - * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
 - * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Other Apparatus Required (optional):

- * Pulse Generator Order Code - 16914: 2 nos. (Optional, to understand the memory operation in dynamic mode).

Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

IT-2013, Ramchandrapura Industrial Area, Sitapura Extension,
Near Bombay Hospital, Vidhani Circle, Jaipur-302022, Rajasthan, India,
Tel: +91-141-2771791 / 2771792; Email: info@tesca.in, tesca.technologies@gmail.com
Website: www.tesca.in