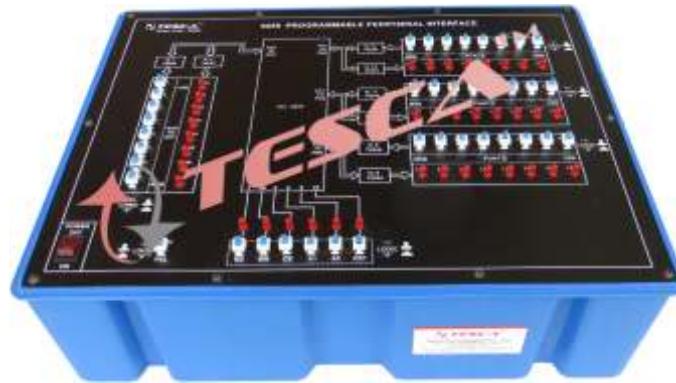


Computer Logic Training Board has been designed specifically for the study of 8255 Programmable Peripheral Interface. This Training Board gives a better understanding of the interfacing between address port & data ports. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.



Object:

To Study 8255 programmable peripheral interface IC for interface operations to configure different ports as input port or output port or in combination of input/output.

Features:

The board consists of the following built-in parts :

01. +5V D.C. at 3A, IC regulated power supply internally connected.
 02. Programmable peripheral interface IC 8255.
 03. Eight Tristate buffer ICs 74LS244.
 04. NAND gate IC 74LS00.
 05. OR gate IC 74LS32.
 06. EPROM IC 2716.
 07. D-flip-flop IC 74LS173.
 08. LEDs for visual indication of status.
 09. SPDT switches for logic selection.
 10. Adequate no. of Electronic Components.
 11. Mains ON/OFF switch, Fuse and Jewel light.
- * The unit is operative on 230V \pm 10% at 50Hz A.C. Mains.
 - * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
 - * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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