

Computer Logic Training Board has been designed specifically for the study of Left & Right Shift Registers & Ring Counter. This Training Board makes the student familiar about the working, practical aspect and design of the same by using inverter & J-K flip-flops. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.



Object:

01. To study right shift register.
02. To study left shift register.
03. To study ring counter.

Features:

The board consists of the following built-in parts :

01. +5V D.C. at 100mA, IC Regulated Power Supply.
 02. Two dual J-K Flip-Flop with preset & clear arrangement.
 03. One inverter (NOT gate).
 04. One press switch for clear & one pulser switch for the clock.
 05. Four switches to preset the Flip-Flops.
 06. LEDs for visual indication of output of each flip-flop.
 07. Adequate no. of other Electronic Components.
 08. Mains ON/OFF switch, Fuse and Jewel light.
- * The unit is operative on 230V \pm 10% at 50Hz A.C. Mains.
 - * Adequate nos. of patch cords stackable from rear both ends 4mm spring loaded plug, length ½ metre
 - * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
 - * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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