Computer Logic Training Board has been designed specifically for the use of students in digital electronic lab. The students can build-up various logic functions and understand their working i.e. Full Adder, Half Adder, Even Parity Check, Odd Parity Check, Exclusive OR and different type of Flip-Flops. These all can be synthesized from the most fundamental logic function NAND. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.

Object:

To study and verify the following:

01. AND/NAND function.
02. OR function.
03. Function F=A. (B + C)
04. Exclusive OR function.
05. Coincidence circuit.
06. Full Adder.
07. Majority logic.
08. Error Detecting Codes.
* Even parity check.
* Odd parity check.
09. Binary storage elements.
10. Set-Reset Flip-Flop.
11 (a) Type 'D' Flip-Flop.
11 (b) Edge Triggered Type 'D' Flip-Flop.
12. J-K Flip-Flop
13. Master Slave J-K Flip-Flop.
14. Type 'T' Flip-Flop.

Features:

The board consists of the following built-in parts :

01. + 5V D.C. at 200mA, IC Regulated Power Supply internally connected.
02. Nine, 3-input AND gates whose outputs is connected to an inverter (NOT gate) to give 3-input NAND gates.
03. A clock generator with a repetition frequency of 5 Hz.
04. Two lamps (LEDs) driver circuits each of which individually drives a lamp (LED).
05. Switches for logic selection.
06. Lamps (LEDs) for visual indication of status.
07. Adequate no. of other Electronic Components.
08. Mains ON/OFF switch, Fuse and Jewel light.
* The unit is operative on 230V ±10% at 50Hz A.C. Mains.
* Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ metre.
* Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
* Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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