

Computer Logic Training Board has been designed specifically to study the compound logic functions like Full Adder, Half Adder, Even Parity Check, Odd Parity Check, Exclusive OR and different types of Flip-Flops. These all can be synthesized from the most fundamental logic function NAND. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.

**Object:**

1. Compound Logic Function.
  - 01 Verification of "AND/NAND" Function.
  - 02 Verification of "OR" Function (inclusive OR)
  - 03 Study of Function  $F=A \cdot (B + C)$
  - 04 Study of Coincidence Function  $F = A \cdot B +$
  - 05 Study of Majority Logic  $F=ABC+AC+AB +BC$
2. To study Exclusive OR.
3. To study Half Adder.
4. To study Full Adder.
5. Error detecting codes.
  - 01 To study Even Parity Check
  - 02 To study Odd Parity Check.
6. Binary storage elements
  - 01 Set-Reset Flip-Flop
  - 02 Type D Flip-Flop
  - 03 J-K Flip-Flop
  - 04 Master Slave J-K Flip-Flop
  - 05 Type T Flip-Flop.



**Features:**

The board consists of the following built-in parts :

01. + 5V D.C. at 100mA, IC Regulated Power Supply internally connected.
  02. Seven, 2-input AND gates each followed by an inverter to give Seven 2-input NAND gates.
  03. Four, 3-input AND gates each followed by an inverter to give four 3-input NAND gates.
  04. One gated Flip-Flop.
  05. A clock generator with a repetition frequency of 5 Hz.
  06. Switches for logic selection.
  07. LEDs for visual indication of status.
  08. Adequate no. of other Electronic Components.
  09. Mains ON/OFF switch and Jewel light.
- \* The unit is operative on 230V  $\pm 10\%$  at 50Hz A.C. Mains.
  - \* Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length  $\frac{1}{2}$  metre.
  - \* Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections /observation of waveforms.
  - \* Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

**Tesca Technologies Pvt. Ltd.**

IT-2013, Ramchandrapura Industrial Area, Sitapura Extension,  
Near Bombay Hospital, Vidhani Circle, Jaipur-302022, Rajasthan, India,  
Tel: +91-141-2771791 / 2771792; Email: info@tesca.in, tesca.technologies@gmail.com  
Website: www.tesca.in