



Experimental Training Board has been designed specifically to study the Forbidden Energy Gap in Semiconductor Diode. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.

Object:

To study the Forbidden Energy Gap in Semiconductor Diode.

Features:

The board consists of the following built-in parts :

- 01. 2V D.C. at 10mA, regulated Power Supply.
- 02. Digital Microammeter, 3½ digits having range 200mA D.C.
- 03. Semiconductor Diode.
- 04. Thermometer 0-110 °C.
- 05. Oven, Electrically heated to heat the Semiconductor Diode.
- 06. Mains ON/OFF switch and Fuse.

- * The unit is operative on 230V ±10% at 50Hz A.C. Mains.
- * Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length ½ metre.
- * Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections /observation of waveforms.
- * Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

Tesca Technologies Pvt. Ltd.

B-201A, Ramesh Nagar, Indraprastha, New Delhi-110028, India
 Near Bombay 262626, Withani Circle, Bhopal-462022, Rajasthan, India,
 Tel: 91-042-271179 tesca technologies@gmail.com, tesca.technologies@gmail.com
 Website: www.tesca.in