



Experimental Training Board has been designed specifically for the study & verification of the laws and network theorems in D.C. circuits. The board is absolutely self contained and requires no other apparatus.

Practical experience on this board carries great educative value for Science and Engineering Students.

#### Object:

01. Verification of Ohm's Law.
02. To draw the V-I characteristics for studying the D.C. behavior of the following :
  - (a) Ideal resistance.
  - (b) Semiconductor diode.
  - (c) Zener diode.
  - (d) Thermistor (NTC Type).
03. To verify Kirchoff's current law and voltage law.
04. Verification of the series & parallel laws for resistances.
05. Verification of Superposition Theorem.
06. Study of potential divider.
07. Verification of Maximum Power Transfer Theorem.
08. To verify Thevenin's Theorem and to find equivalent voltage source circuit.
09. To verify Norton's Theorem and to find equivalent current source circuit.
10. To study the design of a multimeter.

#### Features:

The board consists of following built-in parts:

01. 0-30V D.C. at 100 mA, continuously variable IC Regulated Power Supply.
  02. +9V D.C. at 100 mA, IC Regulated Power Supply.
  03. +5V D.C. at 100 mA, IC Regulated Power Supply.
  04. D.C. Voltmeter, 65mm rectangular dial with switch selectable ranges of 0.5, 1.5, 25 & 50V.
  05. D.C. Ammeter, 65mm rectangular dial with switch selectable ranges of 0.05, 0.5, 5, 50 & 100mA.
  06. Adequate no. of other electronic components.
- \* The unit is operative on 230V  $\pm$ 10% at 50Hz A.C. Mains.
  - \* Adequate no. of patch cords stackable from rear both ends 4mm spring loaded plug length 1/2 metre.
  - \* Good Quality, reliable terminal/sockets are provided at appropriate places on panel for connections / observation of waveforms.
  - \* Strongly supported by detailed Operating Instructions, giving details of Object, Theory, Design procedures, Report Suggestions and Book References.

Note: Specifications are subject to change.

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